

**AMENDMENTS TO THE SPECIFICATION:**

On page 1, before line 1, please insert the following heading:

“TITLE OF THE INVENTION”

On page 1, please replace the title as follows:

“LIGHT EMISSIVE ELEMENT HAVING PIXEL SENSING CIRCUIT”

On page 1, line 4, please insert the following heading:

“BACKGROUND OF THE INVENTION  
(1) Field of the Invention”

On page 1, between lines 10 and 11, please insert the following heading:

“(2) Description of Related Art”

On page 2, line 23, please insert the following heading:

“BRIEF SUMMARY OF THE INVENTION”

On page 4, line 21, please insert the following heading:

“BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWINGS”

On page 5, line 2, please insert the following heading:

“DETAILED DESCRIPTION OF THE INVENTION”

On page 6, line 32 to page 7, line 19, please amend the following paragraphs:

“Fig 4 shows a schematic circuit diagram of a self-compensated (single transistor) ~~current mirror~~ pixel cell 20 ~~known in the art. Such a pixel can be used to embody~~ embodying the invention. The pixel cell 20 has a data line 21, a power line 22, a memory element 23, a drive

element 24 and an emissive element in the form of an OLED 25. Two switches 26, 27 are provided in series between a store point 28 and the data line 22, and the OLED anode 29 is connected to a point 30 in between these switches 26, 27. The drive element 24 is a transistor. The drive switches can also be transistors, either of PMOS or NMOS type.

Conventionally, both switches 26, 27 are ON when the pixel is addressed under control of the controller 50 (column signal fed to the store point 28 and to the OLED anode 29). They are both OFF when the pixel is driving the OLED 25 (voltage provided to the driving element 24 from the memory element 23). This part of the pixel addressing will be employed during the output period.

According to the invention, the pixel is addressed differently during the sensing period. During this period, the first switch 26 is switched OFF while the second switch 27 is switched ON under control of the controller 50. The sensing voltage, which is negative with respect to the OLED cathode voltage 31 is then provided from the data line 21 to the anode 29 of the OLED 25, thereby bringing the diode 25 into reverse mode. This results in a leakage current  $I_L$  flowing through the OLED 25 and through the data line 21, which current can be detected, stored and analyzed, as described above.”

On page 7, line 30 to page 8, line 5, please amend the following paragraphs:

“During the output period, the second switch 33 is OFF, while the first switch 32 is ON during addressing of the pixel, and OFF during driving of the OLED under control of the controller 51.

During the sensing period, the first switch 32 is switched OFF while the second switch 33 is switched ON under control of the controller 51. The negative (with respect to the OLED cathode 31) sensing voltage  $V_1$  is then applied to the OLED 25 from the data line 21, thereby bringing the diode 25 into reverse mode. Again, this results in a leakage current  $I_L$  flowing through the OLED 25 and the data line 21, which current can be detected, stored and analyzed as described above.”